

Bhakta, et al.

Atty Docket: NETL.001DVI

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ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY
MODULE

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Appl. No.: Unknown

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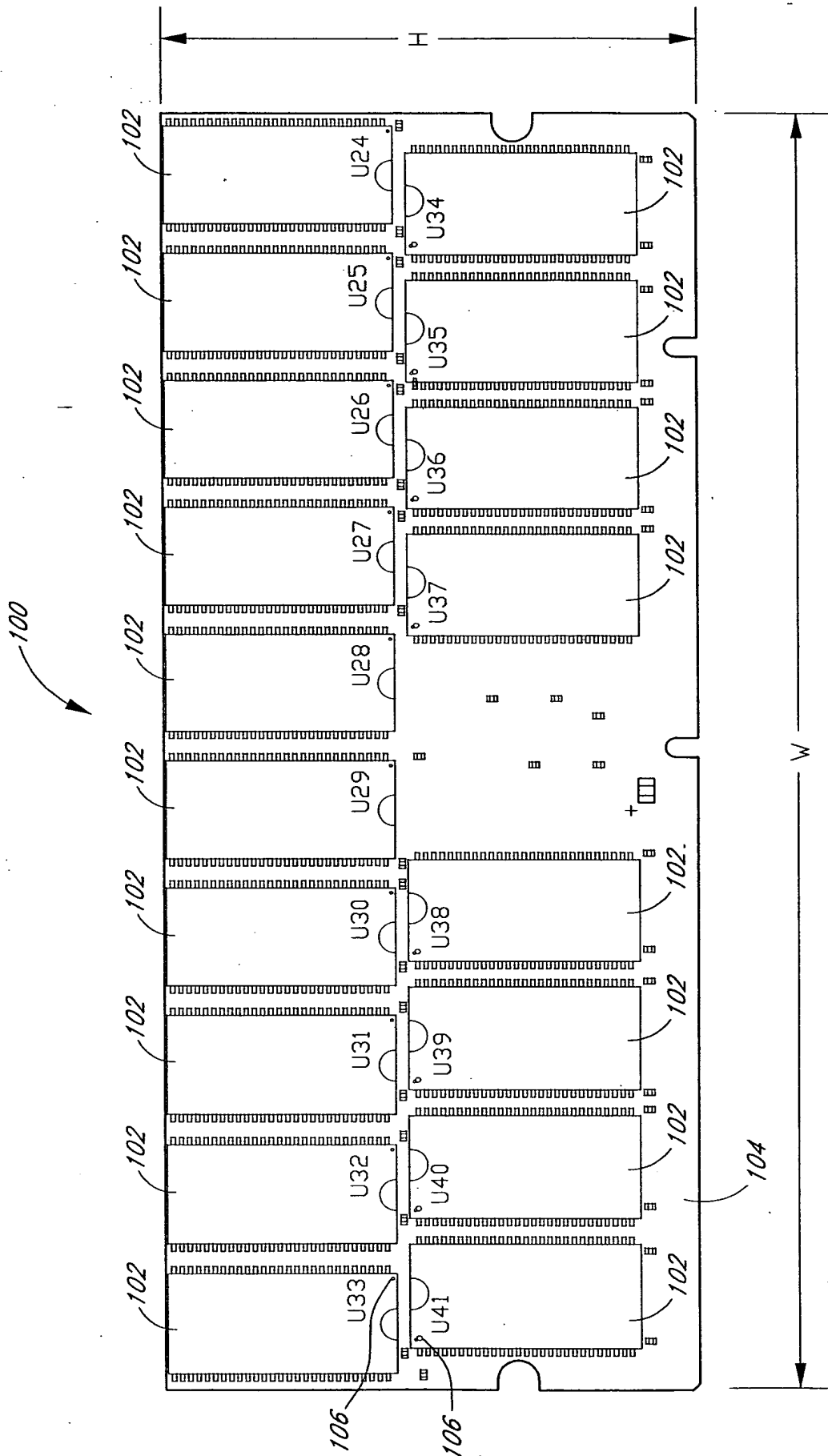


FIG. 1B

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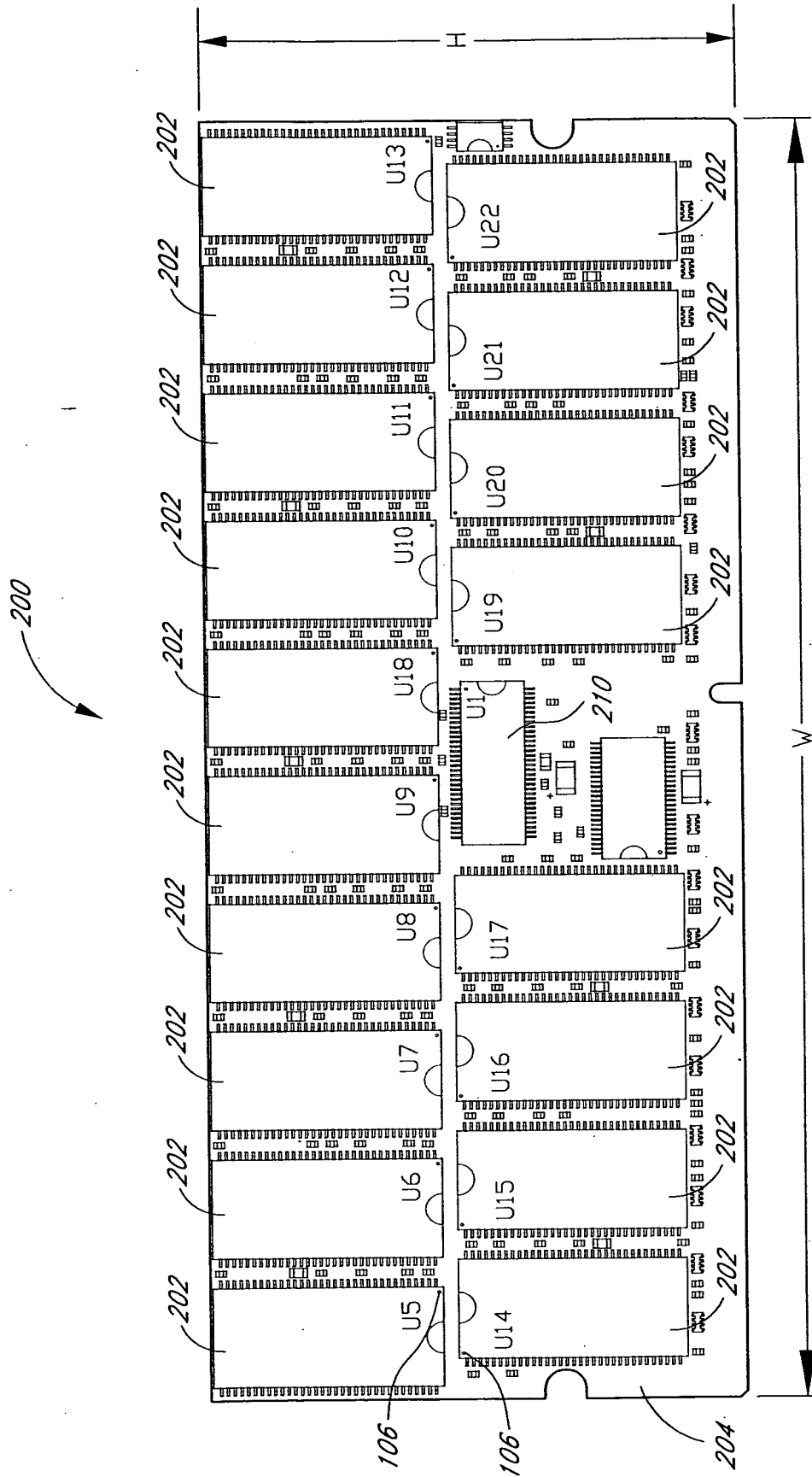
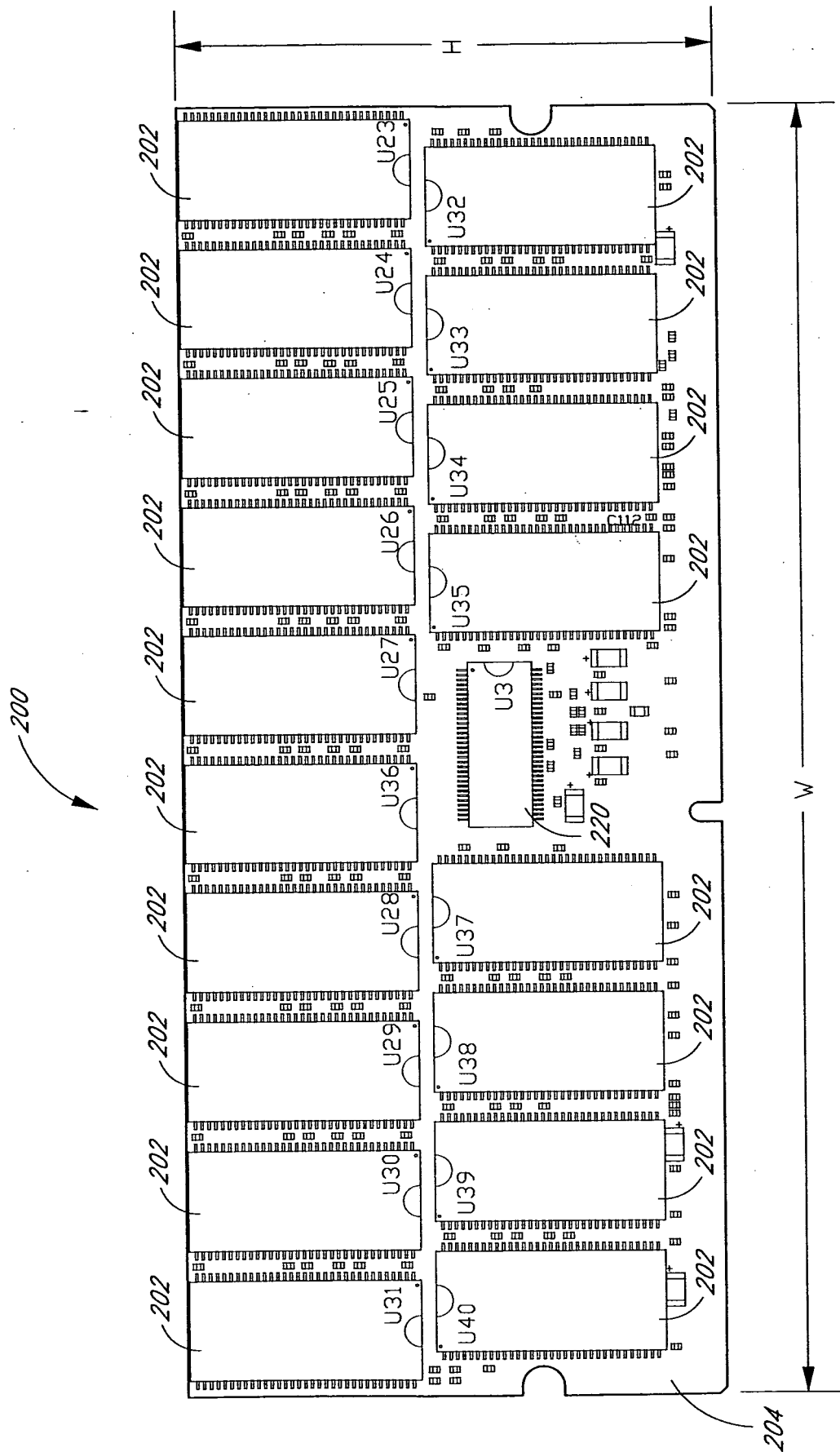


FIG. 2A



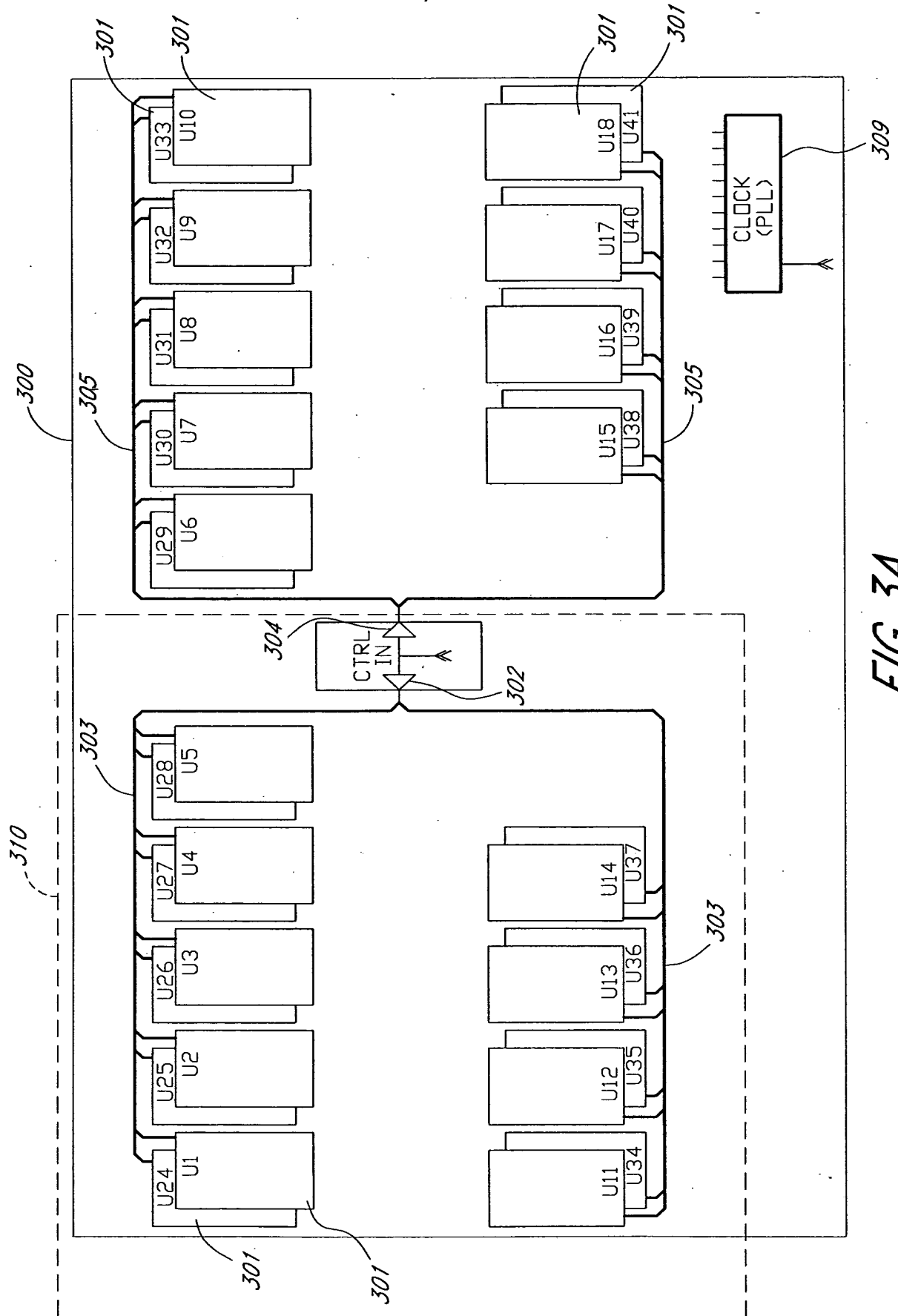


FIG. 3A

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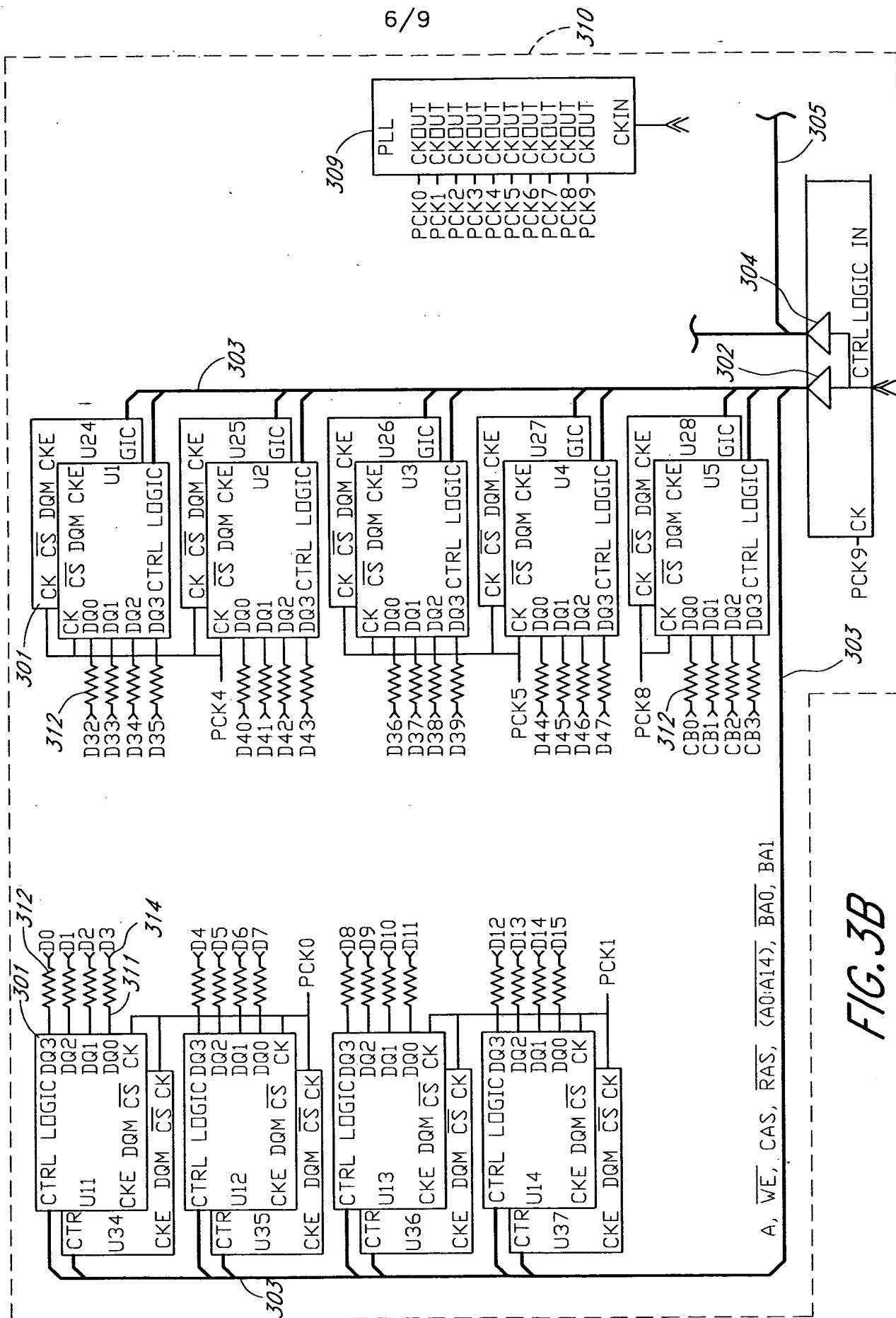


FIG. 3B

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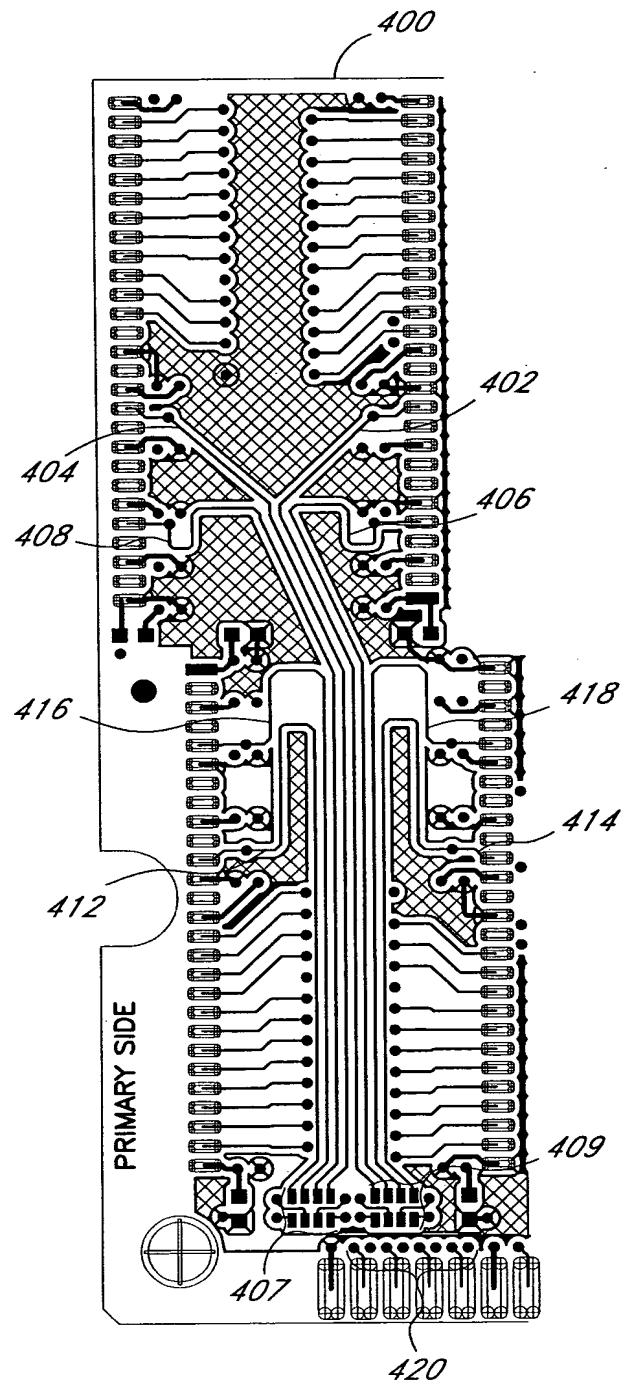


FIG. 4A

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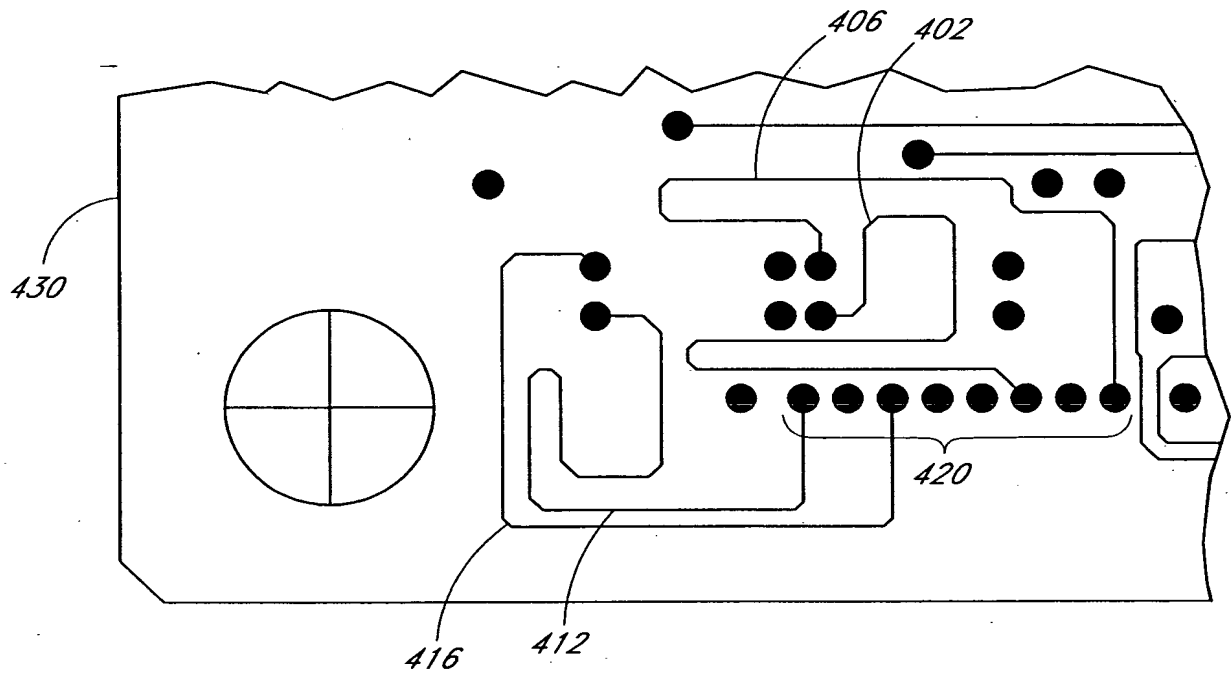


FIG. 4B

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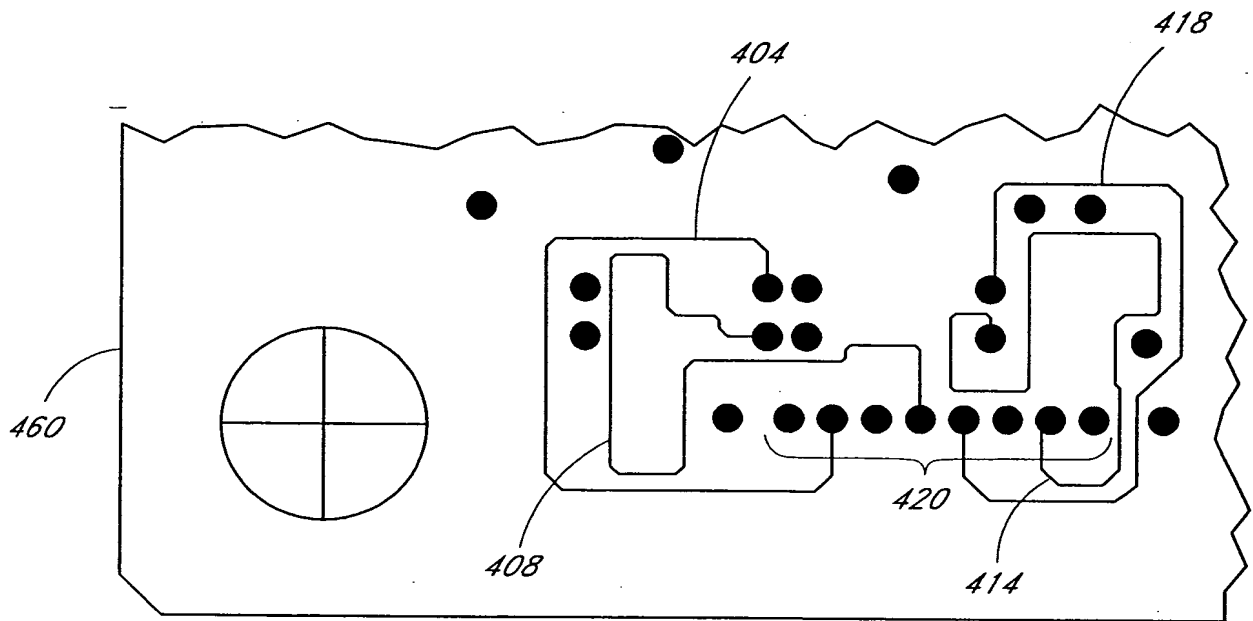


FIG. 4C